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(FILE 'USPAT' ENTERED AT 11:49:17 ON 15 AUG 1999)

L1 23426 SEA (VIDEO(2A) PROCESS###) OR DSP
L2 0 SEA L1 AND (WARPING CARD#)
L3 0 SEA WARPING CARD#
L4 128 SEA WARPING
L5 14 SEA L1 AND (BILINEAR INTERPOLATOR)
L6 6 SEA L5 AND (PLA OR PAL OR PROGRAMMABLE LOGIC ARRAY OR PROG
RAM
L7 22363 SEA (PLURAL OR MULTIPLE OR DUAL) (2A) (MICROPROCESSOR OR COM
PUT
L8 8136 SEA (SHARED OR COMBINED OR PARTITION###) (2A) (MEMORY OR CAC
HE
L9 2222 SEA L7 AND L8
L10 58040 SEA (CONCURRENT OR SIMULTANEOUS OR COINCIDENT OR INSTANTAN
EOU
PER
L11 1318 SEA L9 (P) L10
L12 1318 SEA L10 AND L9
L13 6317 SEA (CONCURRENT OR SIMULTANEOUS) (2A) (PROCESS### OR ACCESSSS

L14 2512 SEA (CONCURRENT OR SIMULTANEOUS) (2A) (ACCESS###)
L15 8501 SEA L13 OR L14
L16 539 SEA L15 AND L9
SAVE ALL VIDPROCESS2/L

FILE USPAT